## In the Specification:

Please amend paragraph 35 as provided below.

[0035] Referring again to Figure 3, when a frame comes into a port group 328, the first dwell of the frame may be placed into a receive data slot 336. The time slot statically assigned to the port that received the frame is utilized to transfer the frame to central memory. For example, assume TS0 from Figure 4 is assigned to this port. The frame preferably may be transferred as soon as TS0 becomes free. For example, TS0 may become free at dwell time 1. According to the table in Figure 4, uplink 304 is used to transfer the beginning of the frame. Thus, the beginning of the frame may be stored into back-end ASIC 320 (Figure 3) at this time ~~by sending~~ and a PUT message <u>sent to the front-end ASIC that is to transmit the frame</u>. The PUT message preferably contains a "SOF (Start of Frame) offset," which is the location in memory the start of the frame resides. The SOF offset includes a chip offset, which indicates the back-end ASIC that contains the start of the frame.